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APPLICATION NO.	FILING DATE	. FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/649,410	08/27/2003	Hans-Georg Mensch	P2001,0132	5309
759			EXAMINER	
LERNER AND GREENBERG, P.A. POST OFFICE BOX 2480		*	HO, TU TU V	
	, FL 33022-2480		ART UNIT	PAPER NUMBER
	•		2818	
•			DATE MAILED: 05/27/2004	,

Please find below and/or attached an Office communication concerning this application or proceeding.

			AVA
•	Application No.	Applicant(s)	
Office Action Summers	10/649,410	MENSCH ET AL.	
Office Action Summary	Examiner	Art Unit	
	Tu-Tu Ho	2818	
The MAILING DATE of this communication Period for Reply	appears on the cov r sheet	with th correspondence addr	ess
A SHORTENED STATUTORY PERIOD FOR RE THE MAILING DATE OF THIS COMMUNICATIO - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above, the maximum statutory per - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the material patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no event, however, may reply within the statutory minimum of triod will apply and will expire SIX (6) Meaning the cause the application to become	a reply be timely filed thirty (30) days will be considered timely. ONTHS from the mailing date of this com	munication.
Status		•	
1) Responsive to communication(s) filed on 22	7 August 2002		·
	This action is non-final.		•
3) Since this application is in condition for allo	· ·	ottora proposition as to the	manisa ia
closed in accordance with the practice unde			nents is
	si Ex parte Quayle, 1955 C	.D. 11, 453 O.G. 213.	* *
Disposition of Claims			
4)⊠ Claim(s) <u>1-5</u> is/are pending in the applicatio	in.		
4a) Of the above claim(s) is/are without	drawn from consideration.		
5) Claim(s) is/are allowed.			**************************************
6)⊠ Claim(s) <u>1-4</u> is/are rejected.			
7)⊠ Claim(s) <u>5</u> is/are objected to			
8) Claim(s) are subject to restriction and	d/or election requirement.		
Application Domesto	, , , , , , , , , , , , , , , , , , ,		
Application Papers	•		
9) The specification is objected to by the Exam	·	•	
10)⊠ The drawing(s) filed on <u>27 August 2003</u> is/ar			
Applicant may not request that any objection to t	•		•
Replacement drawing sheet(s) including the corr			
11) The oath or declaration is objected to by the	Examiner. Note the attach	ed-Office Action or form PTO	-152
Priority under 35 U.S.C. § 119			
12)☐ Acknowledgment is made of a claim for forei a)☐ All b)☐ Some * c)☐ None of:		§ 119(a)-(d) or (f).	•
1. Certified copies of the priority docume			
2. Certified copies of the priority docume		· · · ———	
3. Copies of the certified copies of the p		n received in this National St	age
application from the International Bure			
* See the attached detailed Office action for a I	ist of the certified copies no	ot received.	
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Attachment(s) 1) X Notice of References Cited (PTO-892)	المراجعة	0 (070 (40)	•
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No	y Summary (PTO-413) o(s)/Mail Date	
 Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date <u>08/27/2003</u>. 	08) 5) Notice of 6) Other: _	Informal Patent Application (PTO-1	52)

a top side with a surface;

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DETAILED ACTION

Oath/Declaration

1. The oath/declaration filed on 08/27/2003 is acceptable.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-2 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by Evans et al. U.S. Patent 5,219,712 (related to European Patent Application Publication 0 319 175, which is cited by Applicant).

Evans discloses in the figures, particularly Figure 15, and respective portions of the Specification a semiconductor chip and a method of fabricating a housing for a semiconductor chip as claimed.

Referring to claim 1, Evans discloses a semiconductor chip, comprising:

said top side having a first portion (an outer portion of chip 50 defined by barrier 66) to be covered by a potting compound or encapsulation compound of a housing;

said top side having a second portion (an inner portion of chip 50 defined by barrier 66) to remain free of the potting compound or encapsulation compound;

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a facilitator selected from the group consisting of a material 54 (radiation energy/particle 54) applied thereon and an areal structure 66 formed thereon for defining a variation in a degree of wettability or adhesion characteristics relative to the potting compound or the encapsulation compound for said surface in said first portion and said surface in said second portion, said facilitator rendering wetting, flowing, or adhesion of the potting compound or encapsulation compound in said second portion of said top side more difficult than in said first portion, and promoting an application of the potting compound or encapsulation compound exclusively on said first portion.

Regarding **claim 2**, Evans further discloses that said first portion of said top side includes connection pads and/or bonding wires 60 and said second portion of said top side includes at least one active component 58.

Referring to **claim 4**, Evans discloses a method for producing a housing for a semiconductor chip 50, which comprises:

selecting a potting compound or encapsulation compound (uncured) 62 for forming a housing for the semiconductor chip;

processing a surface of the semiconductor chip in a second portion (an inner portion of chip 50 defined by barrier 66) of a top side thereof with at least one process selected from the group consisting of coating (coating is interpreted broadly) a material 62/54 (uncured resin 62 and radiation energy/particle) and areally structuring to form a configuration 66 thereon rendering wetting, flowing, or adhesion of the potting compound or encapsulation compound more difficult than in a first portion (an outer-portion of chip-50 defined-by-barrier-66)-of the top-

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side, and promoting application of the potting compound or encapsulation compound exclusively on the first portion of the top side; and

subsequently applying the potting compound or encapsulation compound to the top side of the semiconductor chip and thereby:

covering the first portion of the top side with the potting compound or encapsulation compound; and

leaving the second portion of the top side free of the potting compound or encapsulation compound.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. §103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claim 3 is rejected under 35 U.S.C. §103(a) as being unpatentable over Evans for being obvious.

Evans discloses a semiconductor chip as claimed and as detailed above including the second portion but fails to disclose that the second portion comprises a bearing area for a finger. Nevertheless, since Evans does not exclude the use of a bearing area for a finger for the second portion, it follows that at the time the invention was made, it would have been obvious to one of ordinary skill in the art to modify the second portion such that the second portion comprises a bearing area for a finger.

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Allowable Subject Matter

4. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is an examiner's statement of reasons for the indication of allowable subject matter: The cited art, whether taken singularly or in combination, especially when all limitations are considered within the claimed specific combination, fails to teach or render obvious a method for producing a housing for a semiconductor chip having all limitations as recited in claims 4 and 5, characterized in the limitations of claim 5.

Conclusion

- 5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- a. U.S. Patent 6,388,199 to Jiang et al. discloses a semiconductor package including a substrate having a solder resist with a first portion having a first surface tension and a second portion having a second surface tension for promoting encapsulant adhesion in the first portion.
- b. U.S. Patent 5,963,679 to Setlak discloses a fingerprint sensor including an array of electric field sensing electrodes and a driver for providing a coherent drive signal for the array.

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tu-Tu Ho whose telephone number is (571) 272-1778. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DAVID NELMS can be reached on (571) 272-1787. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

TH

Tu-Tu Ho May 19, 2004 David Nelms
Supervisory Patent Examiner

Technology Center 2800